

MuTRiG v2 specification discussion of 13 July 2018

Topics to discuss (tentative):

- Shopping list as discussed in Geneva (from Frank's personal notes):
 - Better separation of analog and digital lines inside chip
 - Fix event rate limit in short data format: requires pipelining of the arbiter and the master/slave counter selection
 - Phase shift between slow and fast clock under control (synchronous reset issue)
 - Test of bonding and potting. Prototype by Peter and/or evaluation board
 - Europractice switched from Cadence First Encounter to Innovus. Requires additional migration effort (2 months?)
 - power up reset circuit testing is still pending
 - Wishlist from Konrad
 - * Sync while operating. Use one sacrificed channel as an external stimulus to get synchronicity
 - * power on reset
 - * The E and T thresholds are highly correlated. E-branch is currently single ended and not differential
 - * Just a diode for temperature measurement
- Discuss scope of next submission, plan and some milestones.