

Low Level RF Workshop 2022

Wednesday, October 12, 2022

Hardware: Hardware (1/2) - Aula (8:20 AM - 9:50 AM)

-Conveners: Kevin Smith

time	[id] title	presenter
8:20 AM	A11 Next generation RF field detection with the carrier-suppression interferometer (CSI))	Dr MAVRIC, Uros
8:50 AM	A10 Digital Low-Level RF control system for Accumulator Ring at Advanced Light Source Upgrade Project	DU, Qiang MURTHY, Shreeharshini
9:10 AM	A13 Status of the uTCA Digital LLRF design for SARAF Phase II	GIL JALDO, Pilar
9:30 AM	A18 LANSCE Digital Low Level RF Upgrade Overview	VAN ROOY, Paula

Hardware: Hardware (2/2) - Aula (10:20 AM - 11:50 AM)

-Conveners: Mark Crofford

time	[id] title	presenter
10:20 AM	A10 Very Low Noise Receiver Technology for Digital Beam Position and Phase Detection	Prof. VALUCH, Daniel
10:50 AM	A11 FPGA implementation of a multi-harmonic cavity controller for the Proton Synchrotron Booster at CERN	BARRIENTOS, Diego
11:10 AM	A10 Status of the LLRF system for ESS related in-kind project by PEG	CICHALEWSKI, Wojciech
11:30 AM	A10 Digital cavity controller for the 20 MHz, 40 MHz and 80 MHz cavities in the CERN PS	WOOLLEY, Ben