Low Level RF Workshop 2022

Wednesday, October 12, 2022

Poster Session: Poster Session - Lichthof (1:50 PM - 4:40 PM)

time	[id] title	presenter
2:25 PM	[8] Consolidation of SwissFEL LLRF system	GENG, Zheqiao
2:26 PM	[6] The Status of LLRF at ATLAS and New Upgrade	LUO, Yong
2:27 PM	[9] RF Performance Characterization of the SLS-2 500 MHz LLRF Prototype in the Lab	KALT, Roger
2:28 PM	[10] Design and Implementation of a Digital Tuning System for 50 MHz Cavities	JURCEVIC, Mario
2:29 PM	[11] CompactPCI Serial Based Generic and Modular Processing Platform at PSI	STEF, Benoit
2:30 PM	[12] Direct Conversion X-Band RF Front End	DIETRICH, Alexander
2:31 PM	[14] DLLRF for ALBA 3rd Harmonic System	GOMEZ, Agustin
2:32 PM	[18] Implementation of LLRF control software outside of DESY using EPICS	BELLANDI, Andrea
2:33 PM	[15] Startup Sequencer for Tuning and Starting up High Power RF into 50 MHz accelerator cavity	Mr STOLL, Matthias
2:34 PM	[16] DIGITAL LLRF FOR THE CANADIAN LIGHT SOURCE	GOMEZ, Agustin
2:35 PM	[17] New Digital LLRF System for CNAO Linear Accelerator	Mr BARICEVIC, Borut
2:36 PM	[19] Status of the ISIS Synchrotron Digital LLRF System	Mr SEVILLE, Andrew
2:37 PM	[20] Development of a Digital LLRF System for RAON SCL3	JANG, Hyojae
2:38 PM	[22] MTCA.4 based LLRF control system for the J-PARC MR	SUGIYAMA, Yasuyuki
2:39 PM	[25] PETRA IV 500MHz LLRF system	HOFFMANN, Matthias
2:40 PM	[44] SRF Cavity Emulator for PIP-II LLRF Lab and Field Testing	SYED, A.
2:41 PM	[26] Progress of Diamond Digital Low Level RF	GU, Pengda
2:42 PM	[27] Summary of the LLRF activities within the recent FLASH shutdown	SCHMIDT, Christian
2:43 PM	[37] SPS bunch-by-bunch phase measurement with µTCA AFCZ FMC	BORNER, Robert
2:44 PM	[29] Performance and Design of a Precision RF Signal Chassis at Los Alamos Neutron Science Center	VAN ROOY, Paula
2:45 PM	[39] Revolution frequency invariant reconstruction of bunch profiles in fixed frequency clock systems	BARRIENTOS, Diego
2:46 PM	[33] LCLS-II LLRF System Checkout Lessons Learned	BENWELL, Andy
2:47 PM	[36] SPS/LHC setting-up tools using Python	DLUGOSZ, Dominika Agnieszka
2:48 PM	[43] LLRF for PolFEL Accelerator	Dr SZEWIŃSKI, Jarosław
2:49 PM	[46] Analog Cavity Emulators to Support LLRF Development	MURTHY, Shreeharshini DOOLITTLE, Larry
2:50 PM	[49] High beta and low beta 650 MHz PIP-II cavity testing at Fermilab 650STC	VARGHESE, P.
2:51 PM	[50] PIP-II 650 MHz Cryomodule Test Stand LLRF System	VARGHESE, P.

2:52 PM	[95] Clock and LO Phase Noise Correlation Effects on RF Sampling	GRZEGRZÓŁKA, Maciej
2:53 PM	[96] Preliminary design of LLRF system for Korea-4GSR	LEE, Yong-Seok
2:54 PM	[97] Master Oscillator to Phase Reference Line Connection with Active Drift Compensation for the European Spallation Source	SIKORA, Dominik
2:55 PM	[62] Development of pulse-by-pulse RF switching in PAL-XFEL LLRF for dual beamlines	HU, Jinyul
2:56 PM	[64] BESSY-II new digital mTCA.4-based LLRF control for the booster upgrade	Dr ECHEVARRIA FERNANDEZ, Pablo
2:57 PM	[65] Controller latency improvements at REGAE	Mr BÜCHLER, Michael
2:58 PM	[69] A New Longitudinal Diagnostic System for CERN’s Antiproton Machines	BARRIENTOS, Diego NOVEL GONZALEZ, Saul ANGOLETTA, Maria Elena
2:59 PM	[71] The ESS cavities dedicated piezo driver evaluation status	CICHALEWSKI, Wojciech
3:00 PM	[70] Synchronization System Overview for the Polish Free-Electron Laser (PolFEL)	SIKORA, Dominik
3:01 PM	[71] Disturbance Observer Application for the Compensation of the Phase Drift of the LANSCE DTL LINAC Solid State Power Amplifier	ROOY, Paula
3:02 PM	[72] Using the Sirepo Platform for Beamline Simulations	EDELEN, Jonathan
3:03 PM	[73] MATLAB Scripts for RF Commissioning at the LANSCE LINAC	VAN ROOY, Paula KWON, SUNGIL
3:04 PM	[76] The LCLS-II Gun & Buncher LLRF Controller Upgrade	SERRANO, Carlos
3:05 PM	[77] The self exited loop cavity field controller and the cavity simulator implemented in MTCA.4.	BUTKOWSKI, Lukasz GUMUS, Cagil
3:07 PM	[78] Next Generation FRIB LLRF Controller	KUNJIR, Shriraj BERNAL, Enrique ZHAO, Shen
3:08 PM	[84] Status of the Helmholtz Zentrum Berlin Sealab LLRF infrastructure	USHAKOV, Andriy
3:09 PM	[85] Experimental characterization of the LLRF system performance at the HiRES accelerator	SERRANO, Carlos FILIPPETTO, Daniele
3:10 PM	[87] Chatter reduction in sliding mode tuner controller using skipping surface	LEEWE, Ramona
3:11 PM	[88] Low Level RF for a Compact, Portable C-Band LINAC	EDELEN, Jonathan
3:13 PM	[91] Narrow Bandwidth Active Noise Control for microphonics rejection in superconducting cavities at LCLS-II	BELLANDI, Andrea
3:15 PM	[96] PIP-II Resonance Control System	LATSHAW, James
3:16 PM	[100] Iterative Learning –Deep Dive	KOSCIELNIAK, SHANE
3:17 PM	[101] Future Plans for the CLS Storage Ring LLRF	BOYLE, Connor
3:18 PM	[109] Electron-Ion Collider Common Platform System Architecture	NARAYAN, Geetha
3:19 PM	[110] Ultra Low Noise Clock Distribution for Electron-Ion Collider Common Platform	Mr SEVERINO, Freddy
3:20 PM	[111] Development and Integration of New Low-level RF System for MedAustron	CERV, Miha