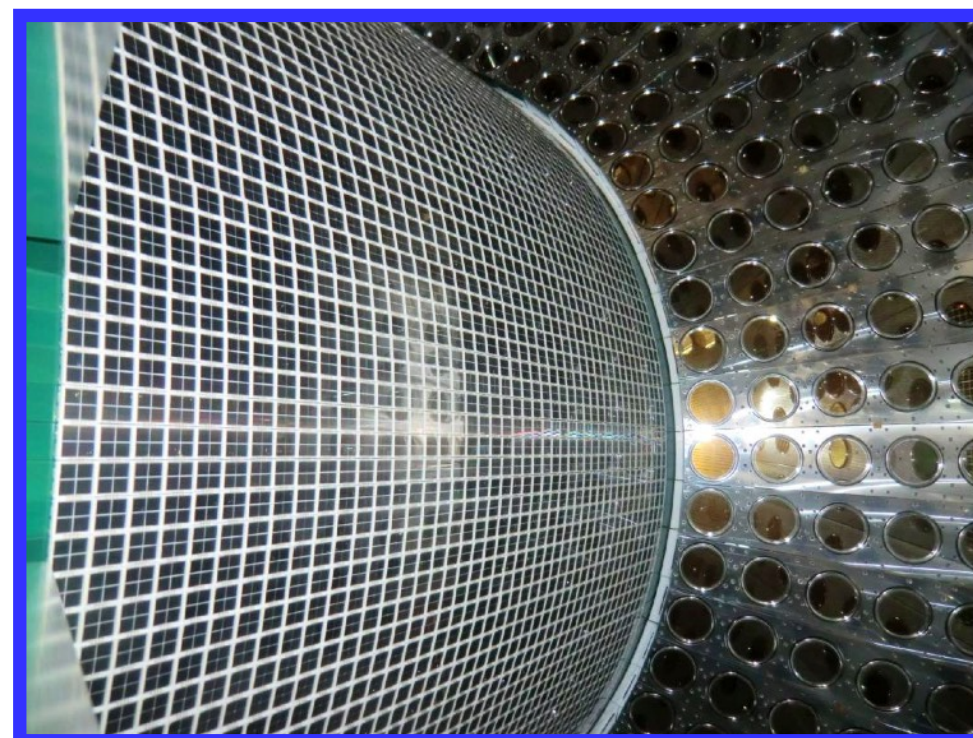


# MIDAS Operation in MEG II Experiment

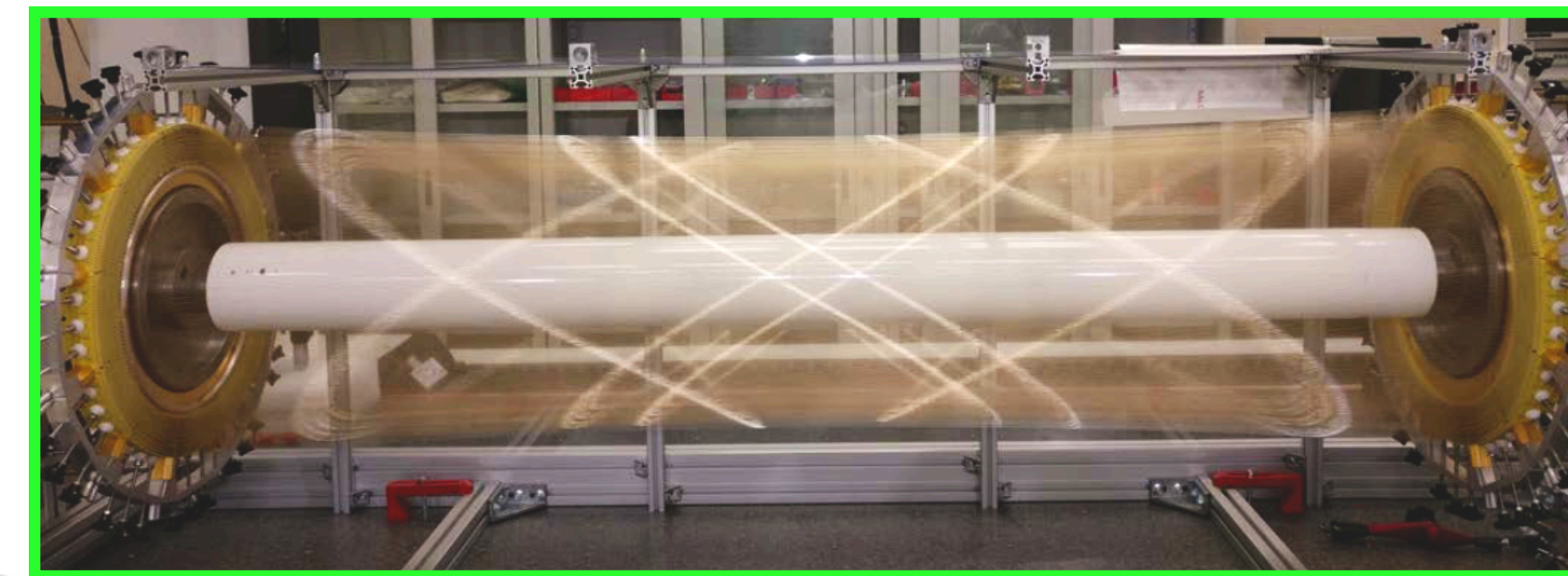
Marco Francesconi  
MIDAS Workshop, ZOOM, 12 Sep 2023

# The MEG II Detectors



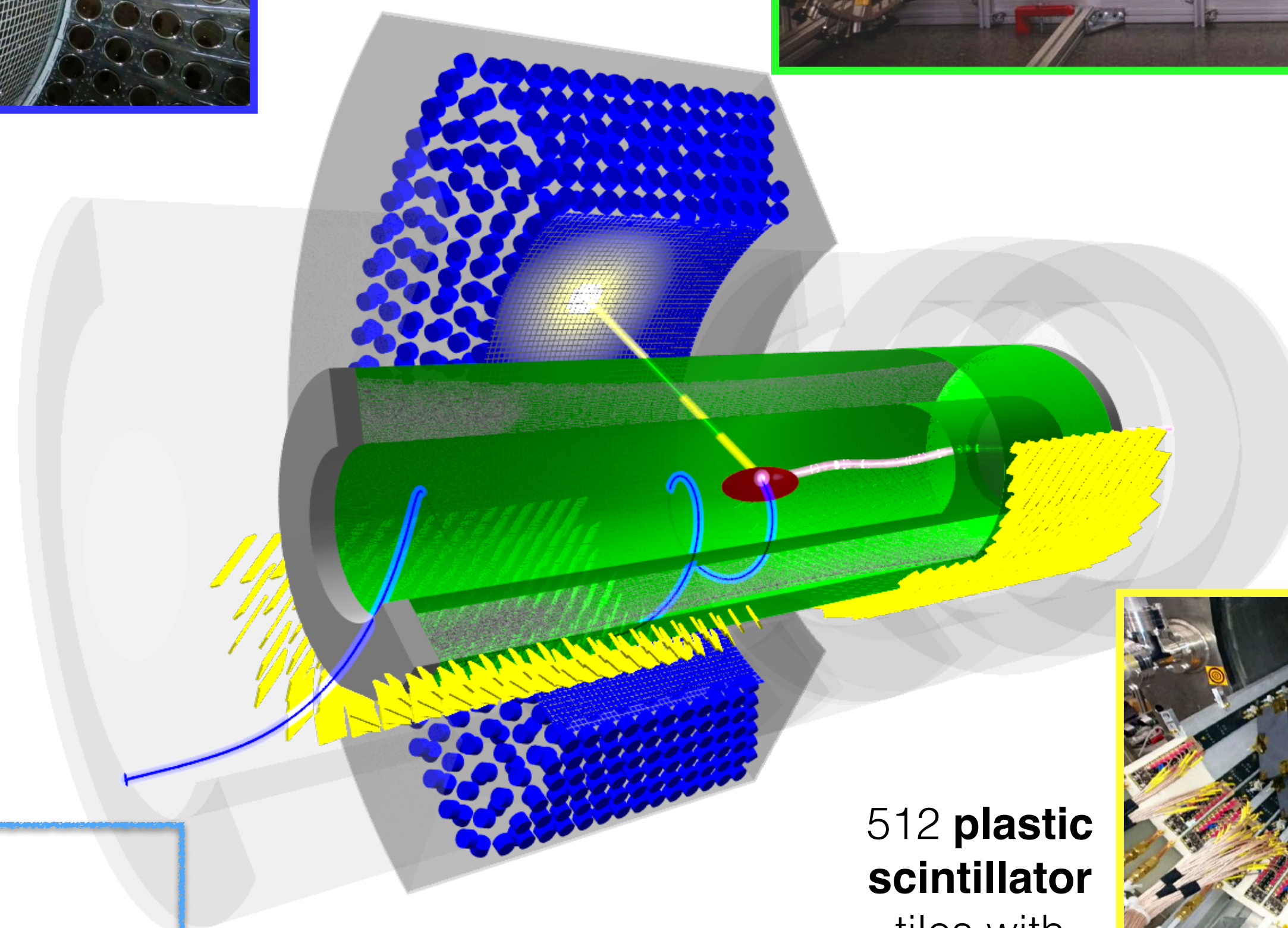
LXe

Scintillation Detector  
4092 **SiPMs** +  
668 **PMTs**



Drift Chamber

1728 square **drift cells**



Timing Counter

MEG Collaboration aims to observe Lepton-Flavour forbidden  $\mu^+ \rightarrow e^+ \gamma$  **decay**

- Impossible to observe in the SM ( $BR \sim 10^{-52}$ )
- Very sensitive to **Beyond SM** physics.

Complementary measurement to  $\mu^- \rightarrow e^-$  conversion (Comet/Mu2e),  $\mu^+ \rightarrow e^+ e^- e^+$  (Mu3e)

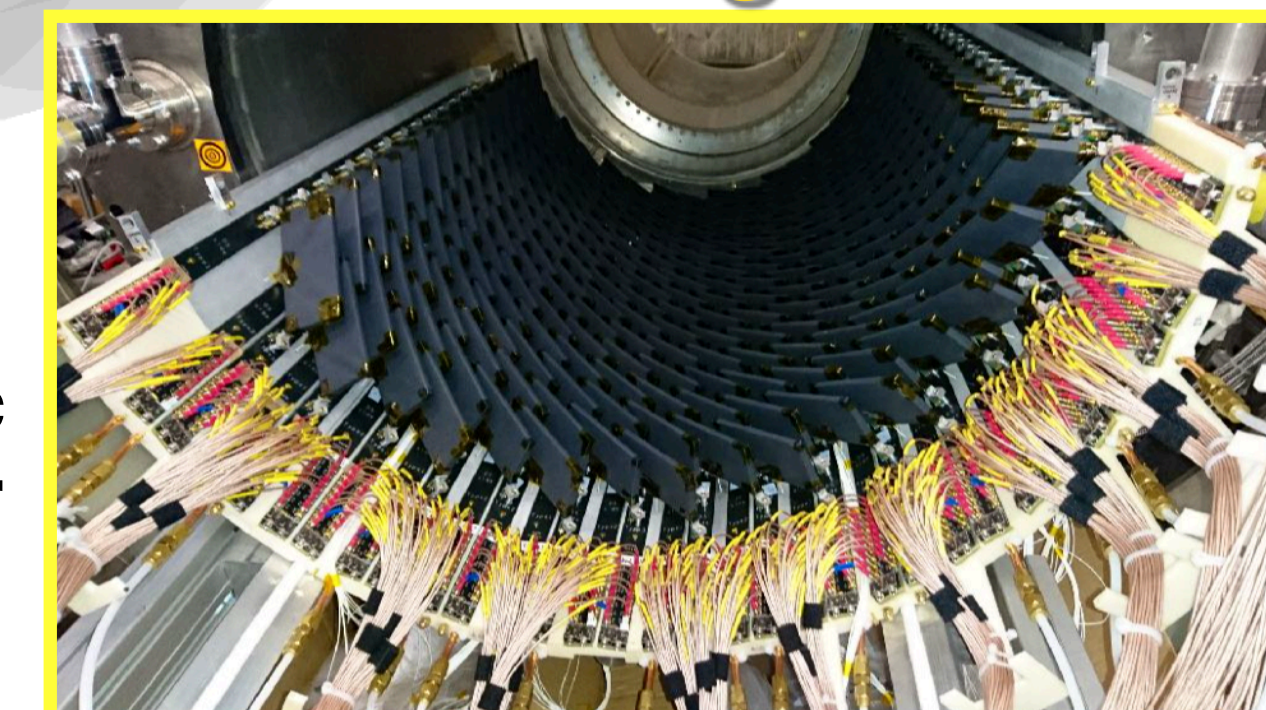
**Detection strategy is the same of MEG**

“Faster, more segmented” detectors

MEG II final sensitivity

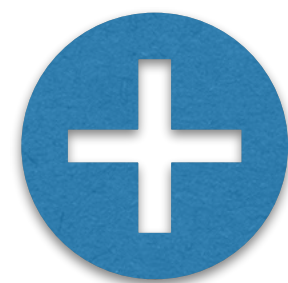
$$BR(\mu^+ \rightarrow e^+ + \gamma) \leq 6 \cdot 10^{-14} \text{ (@ 90\% C.L.)}$$

512 **plastic scintillator** tiles with SiPMs.

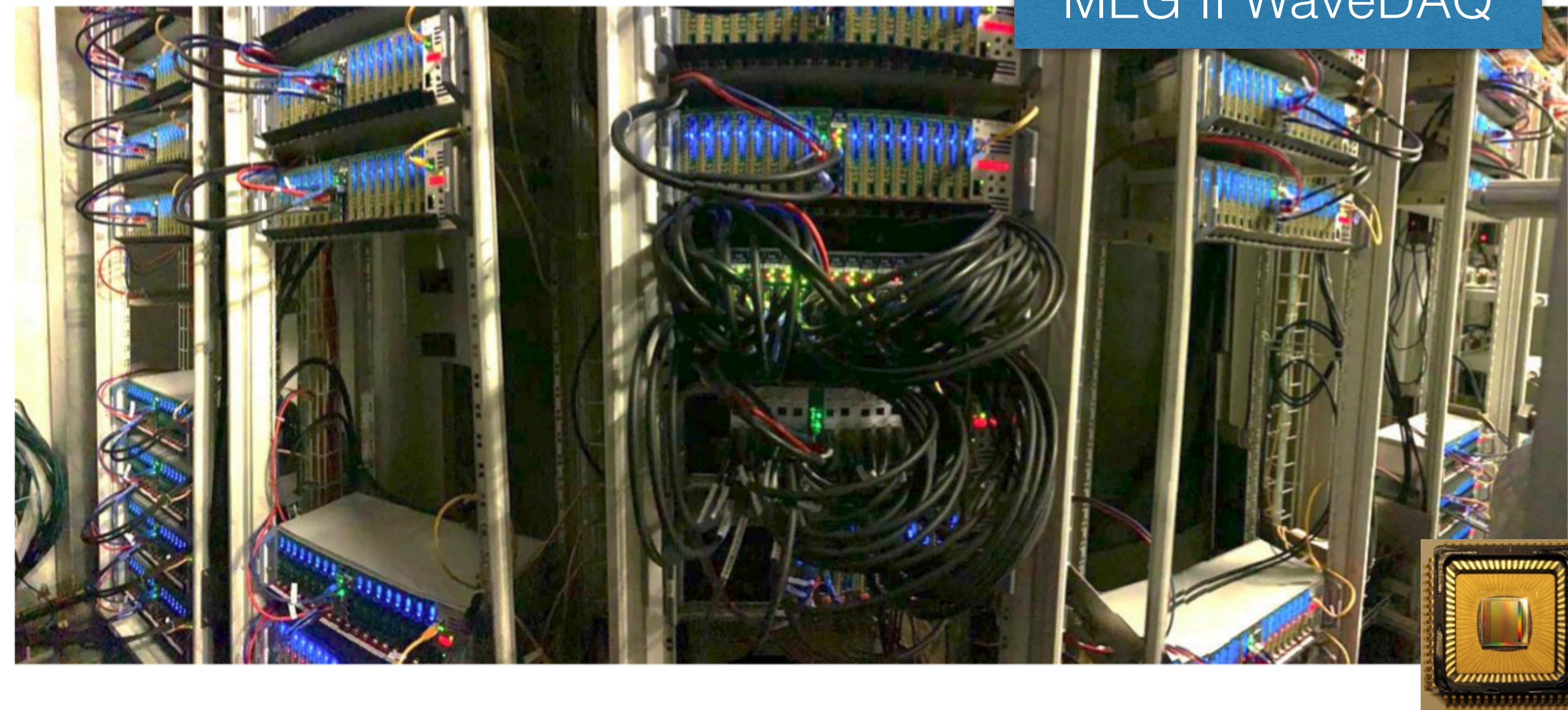
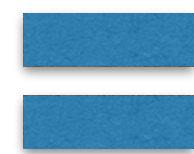
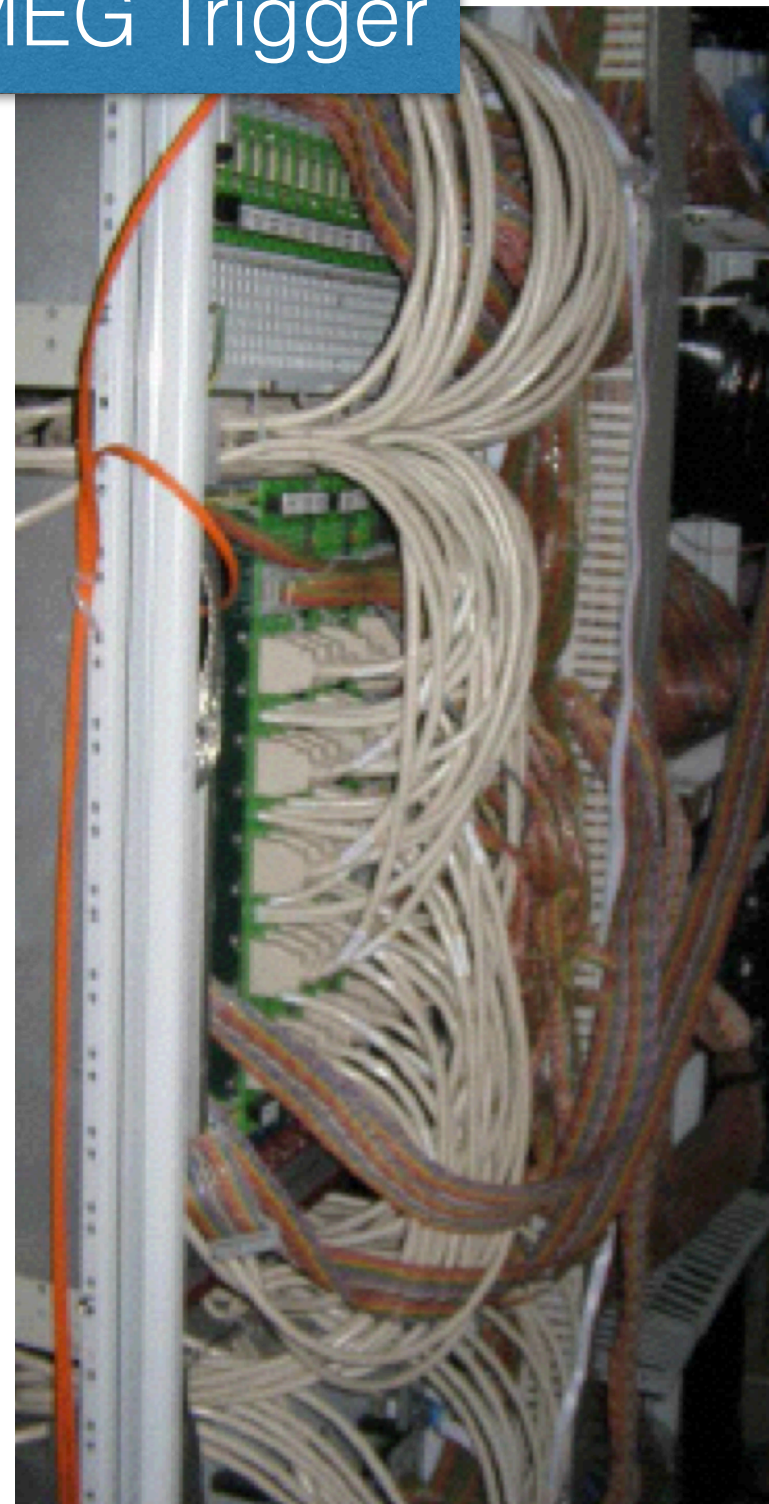


# The MEG and MEG II Trigger and acquisition system

MEG DAQ



MEG Trigger



MEG II WaveDAQ

34 Crates (3U) x 256 Channels/Crate  
"x3 channel density"

5 **VME** Crates (6+3 U) x  
640 DRS4 Channels/Crate

Readout limited due to VME  
Bus

MIDAS VME  
readout  
driver

2 **VME** Crates with  
ADC sampling

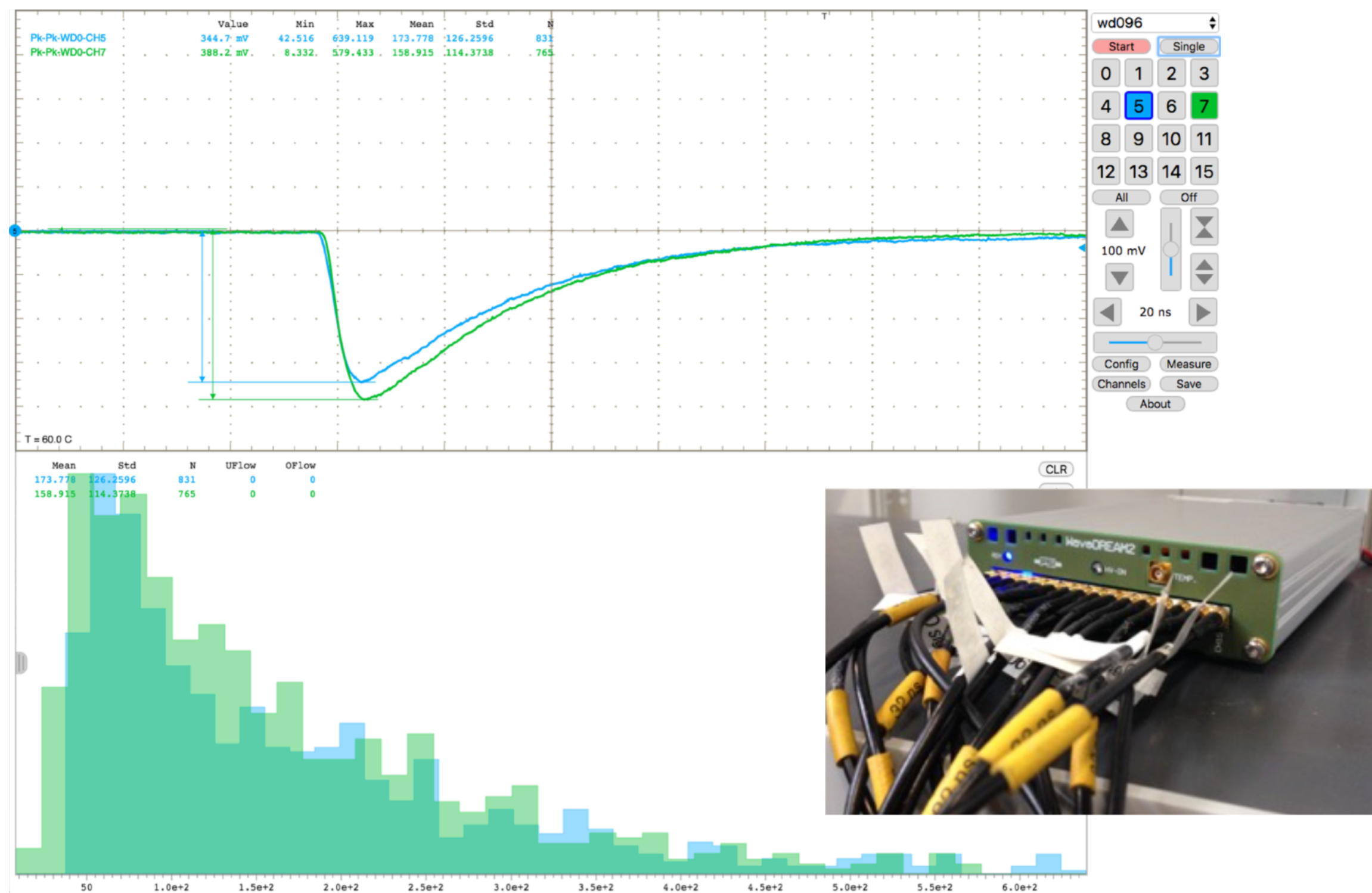
+

1 **VME** crate for  
trigger decision

Consolidation:

- DRS4 everywhere
- SiPM biasing
- Configurable amplification (x0.25-x100)
- Integrating both Trigger and DAQ
- "off the shelf" **ethernet** readout
- Only a single backend server

# WaveDAQ philosophy



Other people (not expert) may use the WaveDAQ boards:  
**Should work “out of the box”**

**Limit the requirements on MIDAS code:**  
only MSC-MXML as git submodules,  
to address slow control facilities (HV settings and Crate control)

All DAQ chain was rewritten  
for **portability** using C++11 std::thread-std::mutex

```
marco ~/Documents/work/git/wavedaq/sw $ git clone ...
marco ~/Documents/work/git/wavedaq/sw $ mkdir build && cd build
marco ~/Documents/work/git/wavedaq/sw/build $ cmake ..
marco ~/Documents/work/git/wavedaq/sw/build $ make install
marco ~/Documents/work/git/wavedaq/sw/build $ ../bin/wds
WDS starting
WDS GIT revision: Tue May 9 12:49:01 2023 +0200 - d913f08 on branch develop
WDS starting HTTP server at port 8080
Listening on UDP port 60916
```

Worked out-of-box also  
on Apple M1

Each board can work as a standalone 16 channel scope  
directly inside a browser  
(WaveDream Server: by Stefan!)  
<http://elog.psi.ch/scope>

Static libraries can then be either used with  
simple “command line” tools or  
integrated in more complex environments

# Integration in MIDAS frontend



Developed before new C++ tmfe frontend interface:  
old mfe.cxx framework

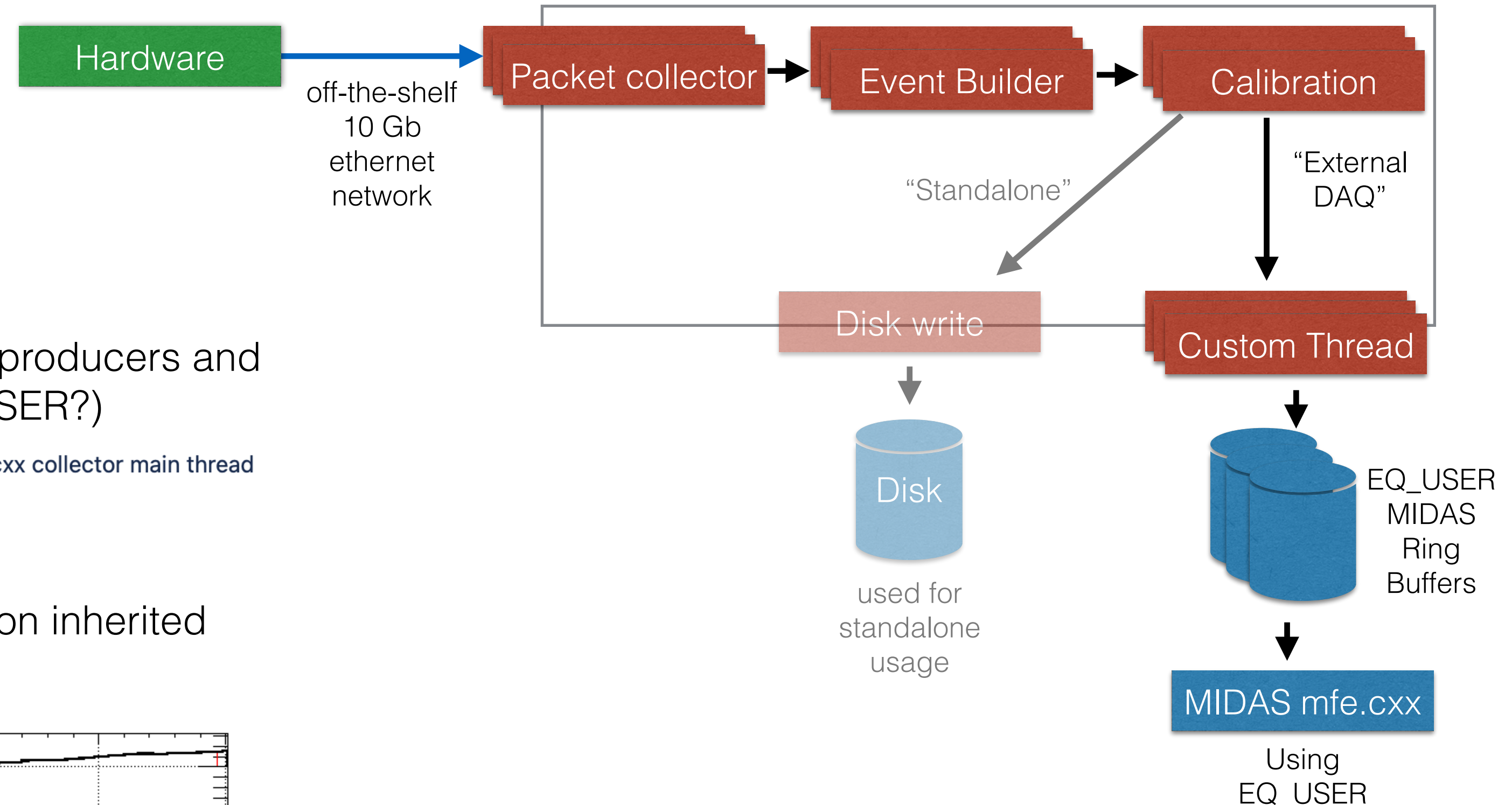
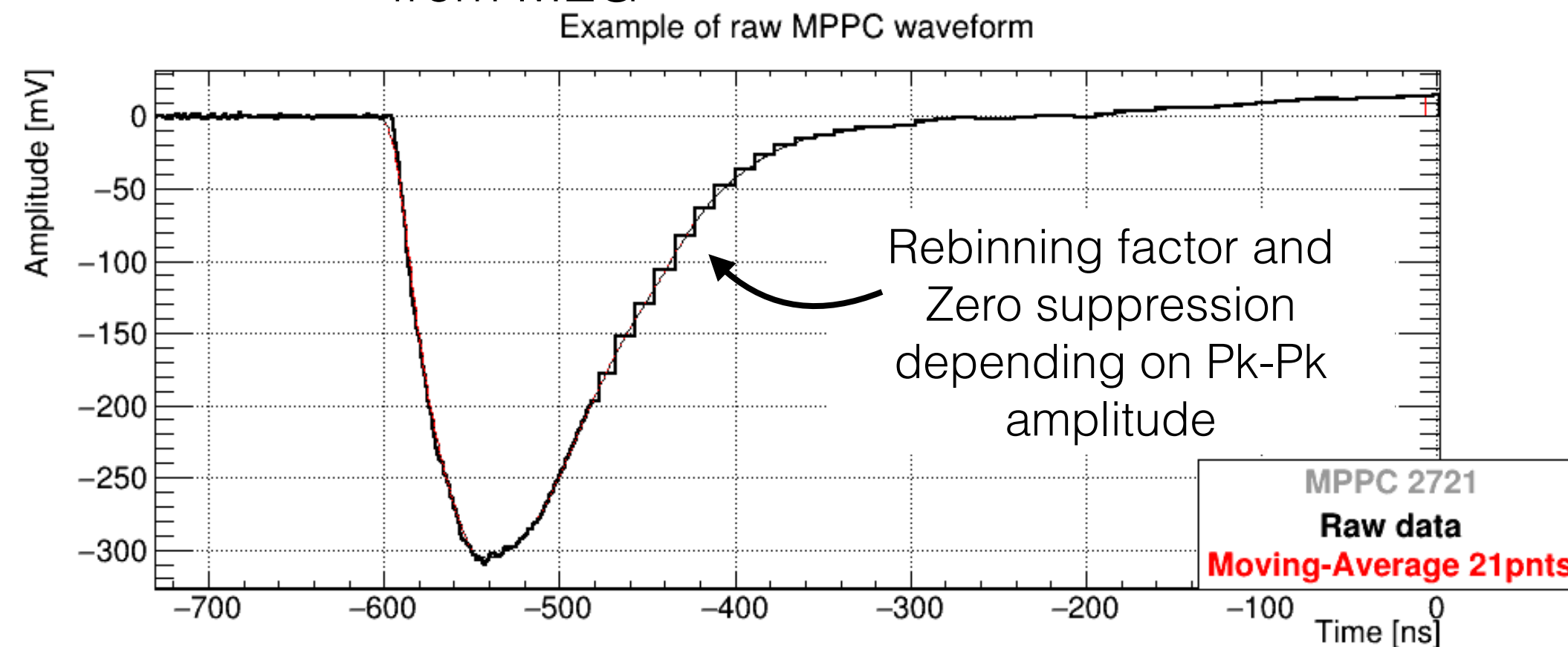
“EQ\_USER”: only expose MIDAS Ring Buffers to user code

Custom interface threads (only piece linking MIDAS libraries)  
write into Ring Buffers, based on mfe.cxx example

Modifications by Stefan to mfe.cxx to support multiple producers and  
thread-safe bank creation (first user of EQ\_USER?)

Stefan Ritt [f52c28e](#) Implemented sorting of EQ\_USER events in mfe.cxx collector main thread

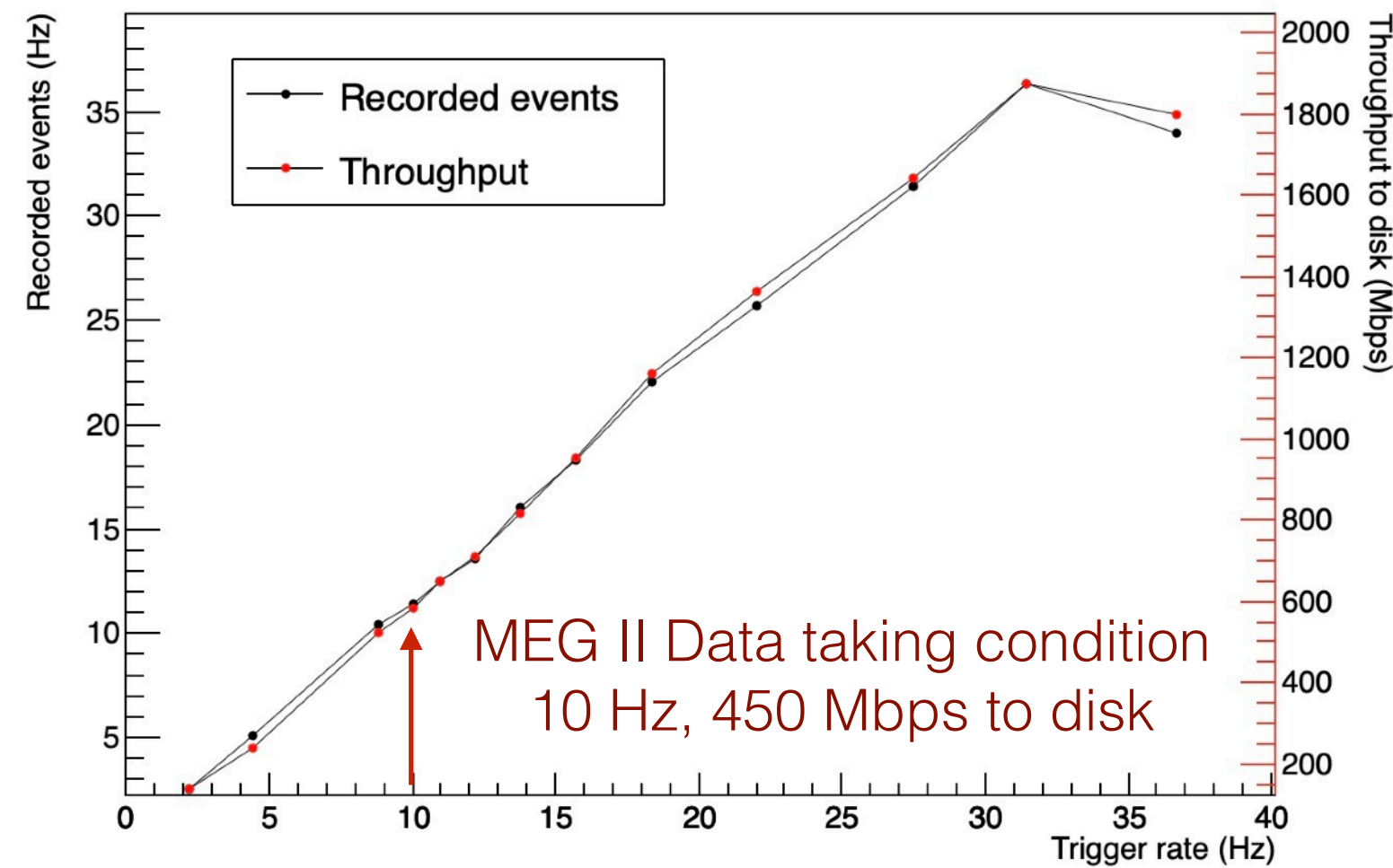
Custom threads also performs also data suppression/reduction inherited  
from MEG



**Data transmission based on UDP:  
packet drops may happens!**

# Performances

Data acquisition performances with full TDAQ, with Logging & Data reduction



## With Logging

NOTE:  
Data compression disabled in mlogger,  
pbzip do not scale enough



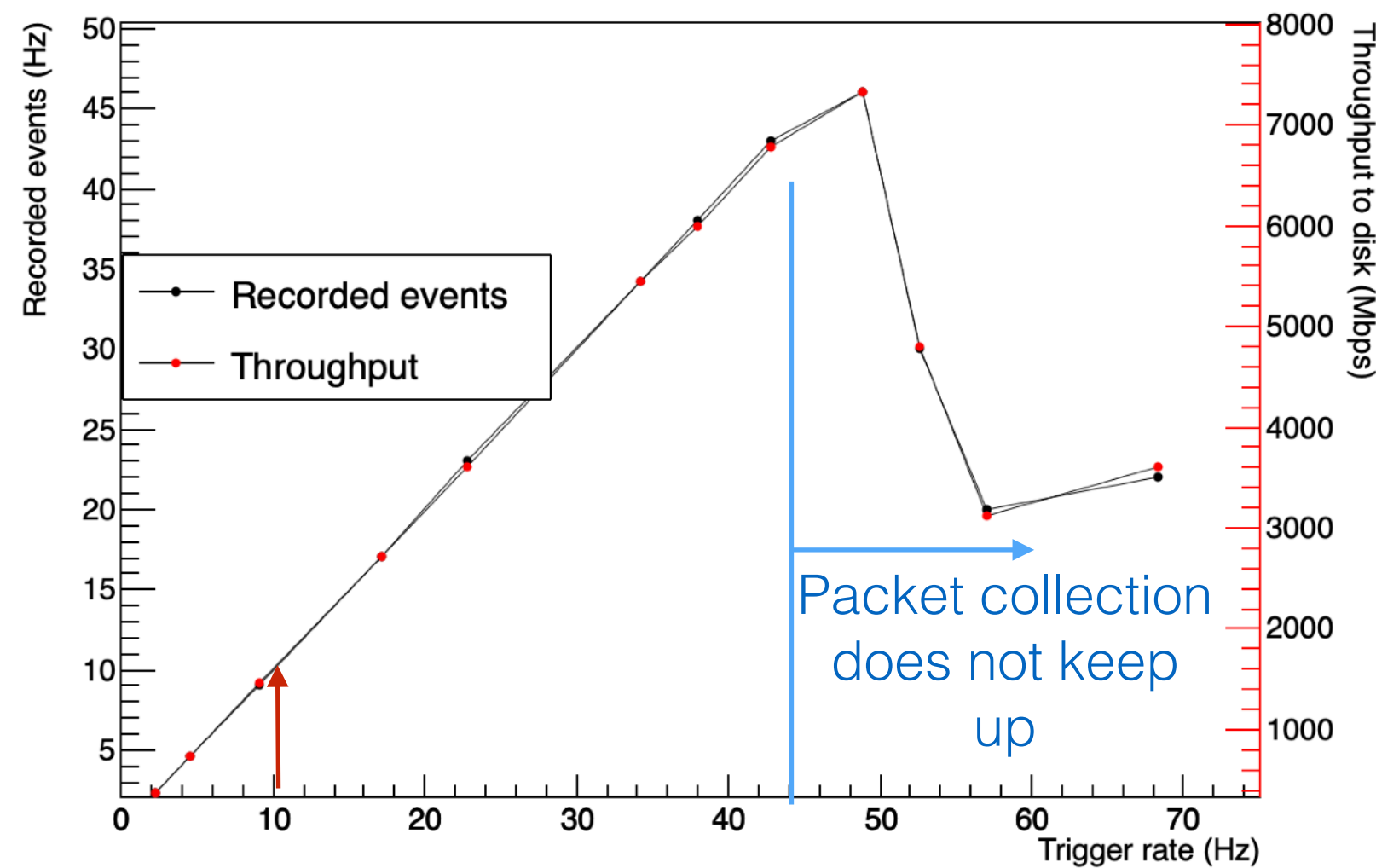
Backend system: 2x Intel Xeon Gold 5218 CPUs @ 2.30GHz  
X710 10Gbps Intel NIC  
1.8TB NVMe SSD

MEG I: Farm of 10 nodes + 1 event builder

## Takeaway message:

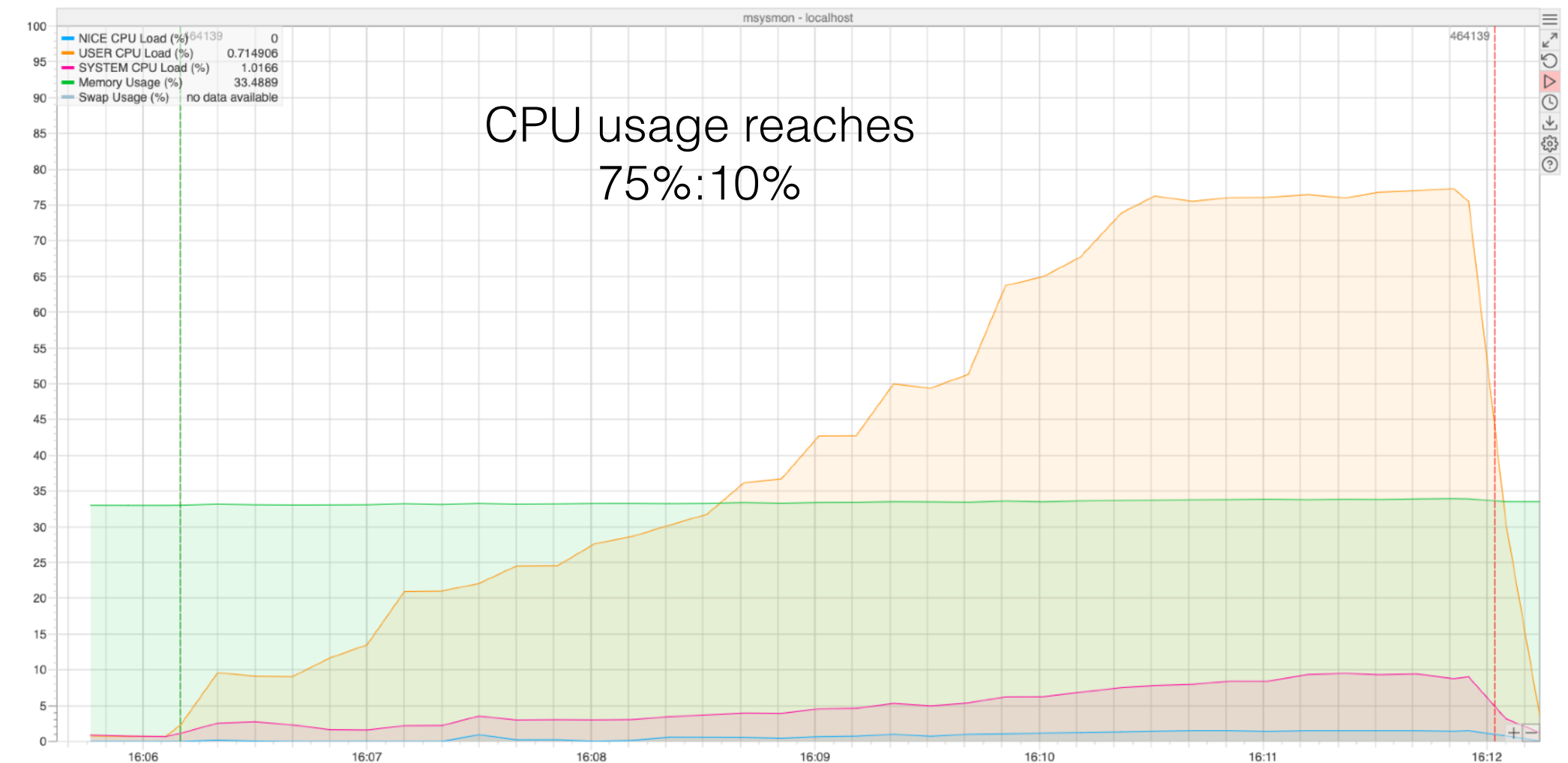
beyond 1 Mpacket/s linux network stack show its limits

Data acquisition performances with full TDAQ, no Logging



## Without Logging

At ~50 Hz hardware  
limit on the  
WaveDAQ side

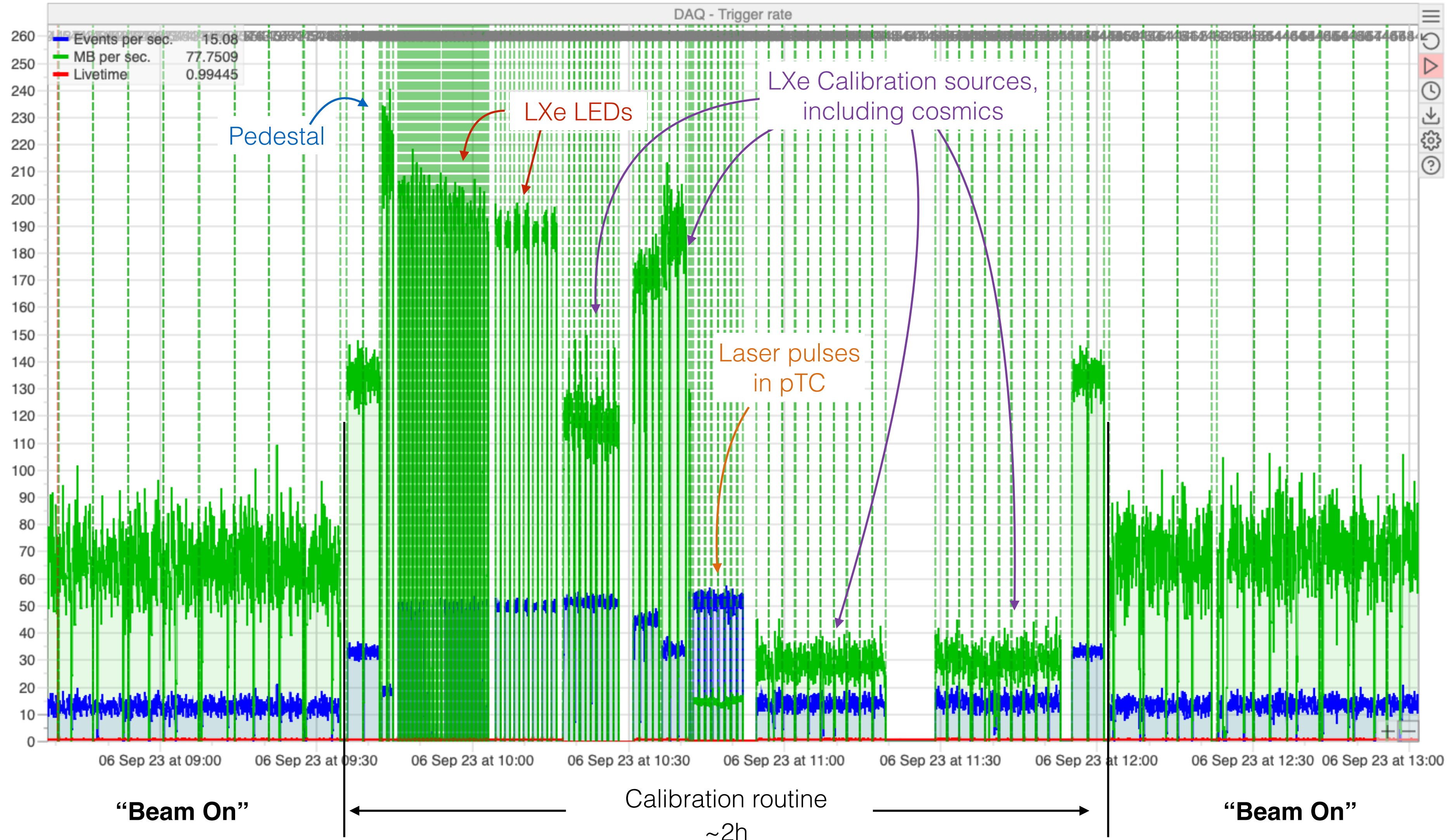


# Why DAQ speed matter

**Faster DAQ**  
↓  
**Quicker calibration**  
↓  
**More “Beam On” statistics**

Currently can digest all data produced up to 50 Hz during calibration

**Do not overengineer:**  
x5 wrt physics needs is reasonable





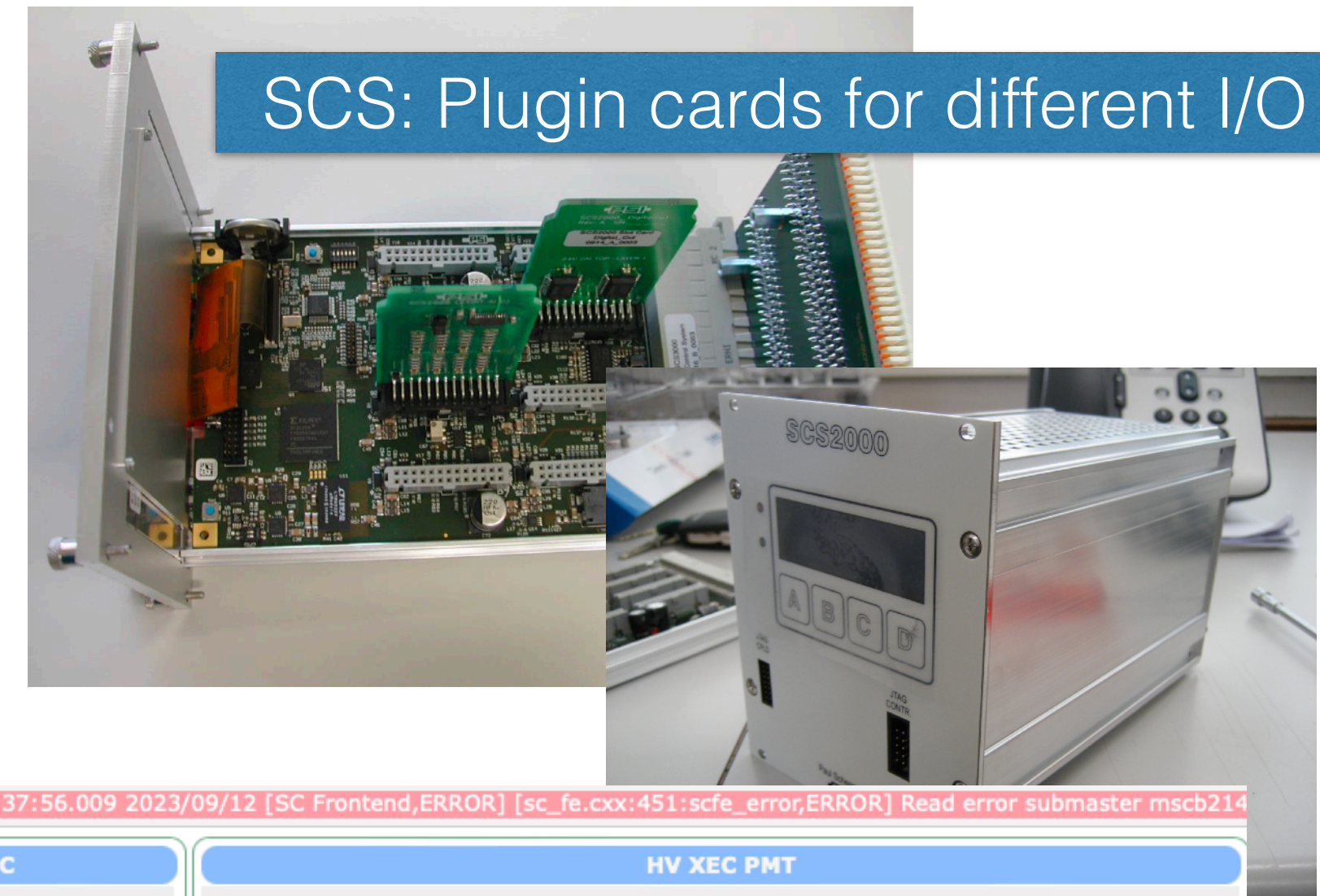


# Slow control

Large use of SCS2000 and SCS3000 from PSI using **MSCB** over ethernet

- More than 20 units in use (monitoring & HW loops)
- +34 MSCB endpoints for the WaveDAQ crates
- +3 MSCB endpoints for old PMT HV dividers
- +EPICS for beamline control
- +few non-standard device (ISEG, Raspberry Pis, LXI)

**600 GB of MIDAS History files since 2015**



CDCH Gas system



CDCH Slow control



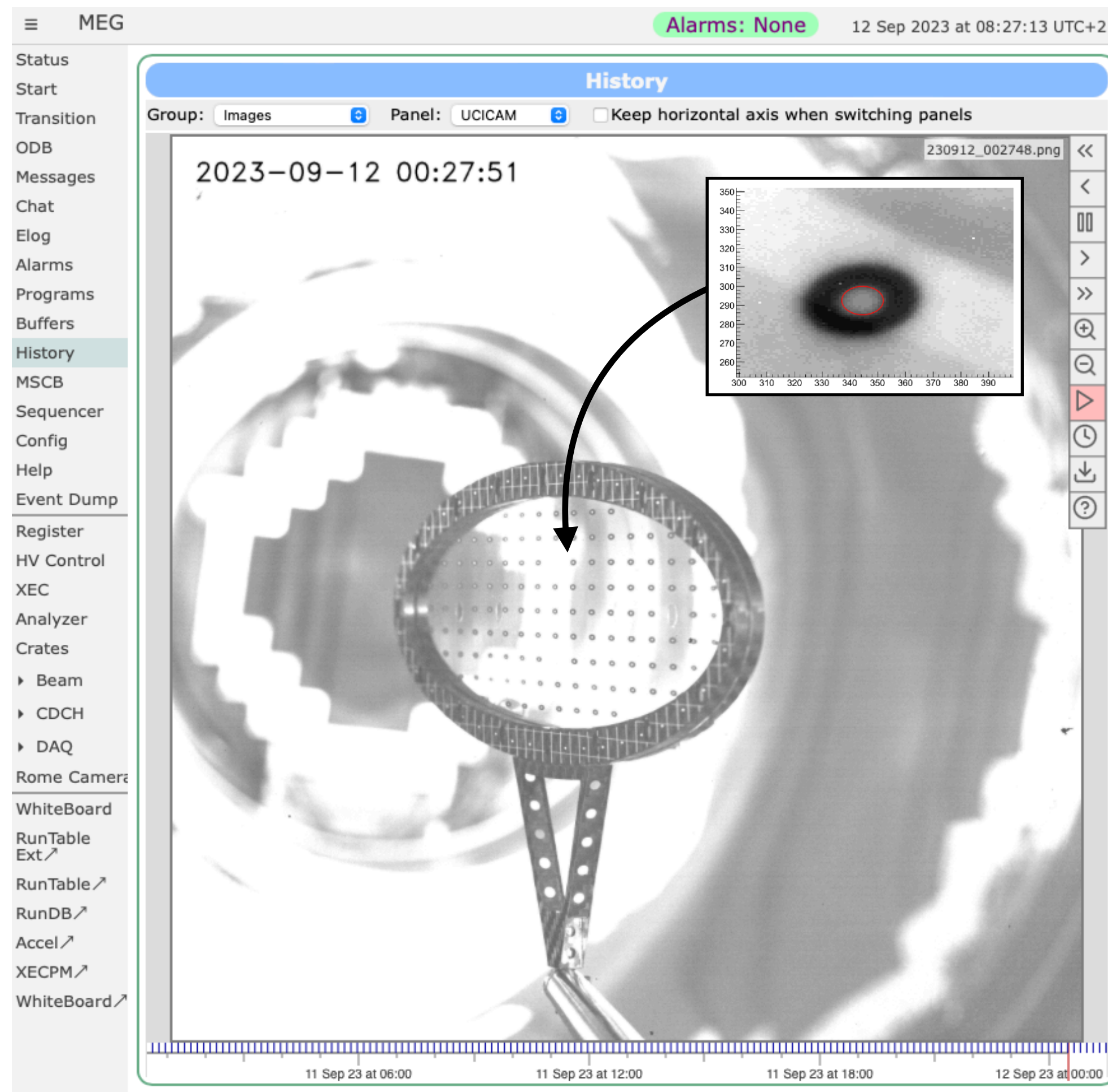
LXe cryogenics slow control



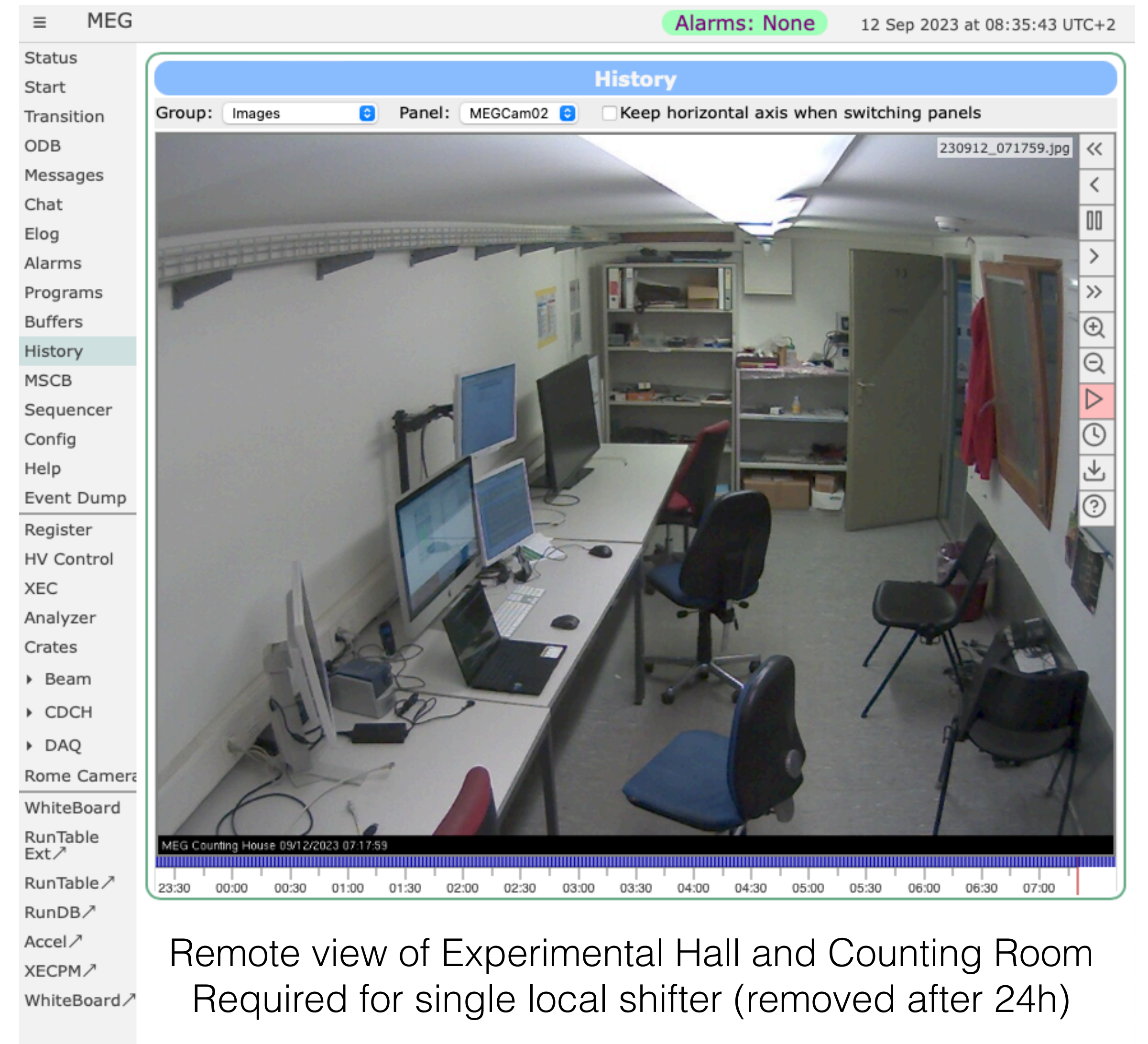
Large use of custom pages for monitoring and action:  
**Very rare need for a shifter to use SSH**

# Image History, use cases

Previously using custom frontend (fetching images) + custom page (remote view)



Photogrammetric measurement of target position and deformation



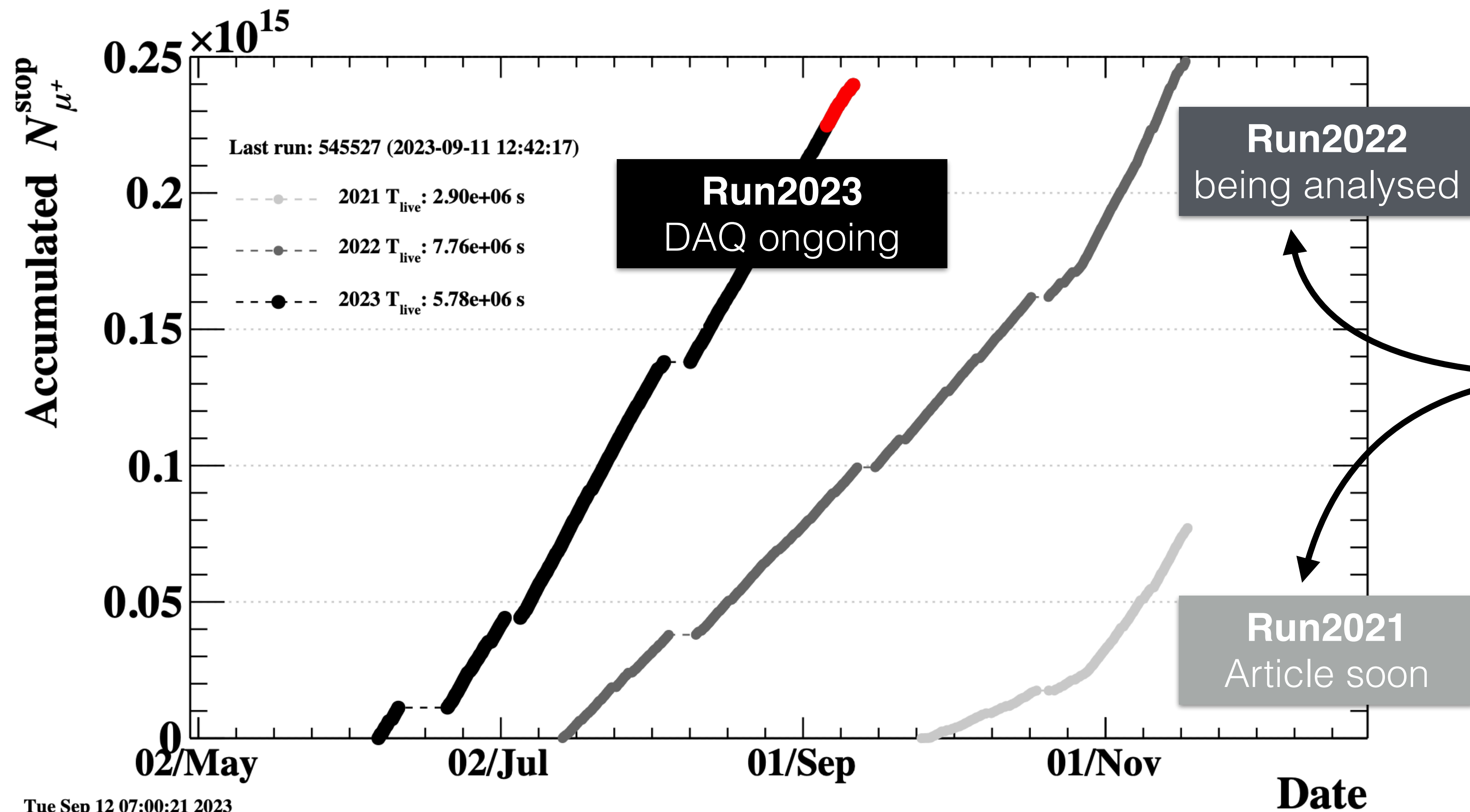
Remote view of Experimental Hall and Counting Room  
Required for single local shifter (removed after 24h)

# Conclusions

MEG II is currently taking data, approximately 500 TB/Run (100 TB/month)

Experiment commissioning and first physics runs during covid!

DAQ expected to end in 2026



During covid time: remote shifts were crucial!