Slim edges in double-sided silicon 3D detectors

M. Povoli^{1,2} A. Bagolini³ M. Boscardin³ G.-F. Dalla Betta^{1,2} G. Giacomini³ E. Vianello³ N. Zorzi³

¹Dipartimento di Ingegneria e Scienza dell'Informazione University of Trento, Italy

> ²INFN Sezione di Padova Gruppo Collegato di Trento

³Centro per i Materiali e i Microsistemi Fondazione Bruno Kessler (FBK), Trento, Italy

iWoRID2011, 3-7 July 2011 ETH Zurich, Switzerland [Work supported by INFN CSN V, projects "TREDI" (2005-2008) and "TRIDEAS" (2009-2011)]







Outline

3D Detectors

Full 3D Simplified approaches - Trento and Barcelona

Slim edge description

Issues with active-edge Slim-edge - General Idea

Slim edge numerical simulation and design

Type of simulations performed Simulations results and layout design

Electrical characterization

Evaluation of the shielding effectiveness

Functional characterization

Laser scans of the edge region



Full 3D detectors Original idea - PRO and CONS



[First proposed by S. Parker et. al. in NIMA 395 (1997), 328]

ADVANTAGES

- 1. Possible to decouple the active volume from the electrode distance
- 2. Very low full depletion voltage (<10V)
- 3. Very small collection distance (designer choice \sim 50 μ m)
- Lower trapping probability after irradiation (shorter collection distances)
- 5. Small dead area along the edges thanks to the active-edge

DISADVANTAGES

- Non uniform response (low field regions present)
- 2. Higher capacitance (Lower S/N)
- 3. Complex fabrication process



Simplified approaches - Double Sided Double Type Columns DDTC - FBK (Trento) and CNM (Barcelona) [3D Processing collaboration]





ADVANTAGES

- 1. Much simpler fabrication process:
 - No need for support wafer
 - Columns are empty
- 2. Very good mechanical yield
- 3. Good electrical yield
- 4. Technologies of choice for the ATLAS-IBL qualification

DISADVANTAGES

- 1. Difficult to fully control the column depth
- If the distance "d" is too long (> 25μm) performances after high irradiation are very poor
- **3.** Empty columns \Rightarrow dead regions
- Not Full 3D ⇒ active-edge still missing



Issues with active-edge



[C.J. Kenney, IEEE TRANSACTIONS ON NUCLEAR SCIENCE, VOL. 48, NO. 6, DECEMBER 2001]



Active-edge and its implications

- Doped trenches around the detectors by means of Deep Reactive Ion Etching (DRIE)
- Strong reduction of the dead edge area
- Unfortunately it causes several complications in the fabrication process:
 - Support wafer needed
 - Backside of the sensor wafer is not accessible
 - The active-edge is not compatible with a double cided presses

a double-sided process

Solution

Different approach aimed at reducing the edge dead area \Rightarrow SLIM-EDGE

NOTE:

A set of planar detectors with active-edges was also processed at FBK in view of the realization of full 3D detectors, several difficulties were encountered although the final result was good

-[M. Povoli, RESMDD10, Firenze, October 2011] -[A. Bagolini, Trento Workshop 2011, Trento, March 2011]

Active-edge vs. Slim-edge General idea



ACTIVE-EDGE

- Dead region along the edge can be reduced down to 20 - 50µm
- 100% edge sensitivity up to few microns away from the physical edge
- ► Support wafer needed ⇒ difficult fabrication process

SLIM-EDGE

- Consists in reducing the edge dead area without making use of the active-edge
- Diamond saw still needed
- Cut region/junction electrode distance?
- Terminating structures?
 - Planar detectors: planar guard-rings (0.5-1mm)
 - Not effective for 3D detectors

Numerical simulations Idea and type of simulations performed

IDEA

Design an ohmic column "fence" able to shield the last junction electrodes from the current coming from the cut region

REQUIREMENTS

- Minimize the edge dead area (~ 200µm)
- Operating voltages > 200V
- Compatibility with a double-sided 3D process (no support wafer)



Numerical simulations

- Modeling the scribe with a with a low lifetime region (τ < 1ns)</p>
- Bias voltage scan (up to very high voltages)
- Monitoring the current of the last junction column
- Avalanche models not active to be sure to only observe edge contributions
- No signs of early discharge should be noticed!



Simulations results

Holes density $[cm^{-3}]$ - Lateral edge - V_{bias} = -300V



Simulations results

Holes density $[cm^{-3}]$ - Top edge - V_{bias} = -300V



Final layout design - Three different devices

ATLAS FEI4 PIXEL

CMS PIXEL

3D DIODE (80µm pitch)



General characteristics

- Long edge: $400\mu m$ ($200\mu m$ for the 3D diode)
- ► Short edge: 200µm
- ALL THESE LAYOUTS ARE CONSERVATIVE... (see next slide)



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Electrical characterization

- The goodness of the implemented layout was tested on 3D diodes
- Several cuts repeated, each one closer to the active area
- Last cut takes away the last row of ohmic columns of the active area



Comments

- First test on ATLAS07 devices: low breakdown, not conclusive
- Second test on ATLAS09 devices: higher breakdown, same behaviour
- Possible to reduce the slime-edge to a total of $\sim 100 \mu m$



ATLAS09 - Higher breakdown



Laser scans of the edge area Measurement setup





Cf=15pF SCOPE Rf=10MΩ ~~~ CB-111 R2 C2 R1 Vbias Vout C1 ∔ Optical iber det. Laser Collimator

System

- > 980nm laser (spot size $\sim 10 \mu m$)
- Motors and positioning system (Thorlabs)
- 3D diode, CMS layout (ATLAS07)
- Cremat CR-111, Charge Sensitive Amplifier
- Tektronix 3052B scope
- PC to acquire data and move the laser



Laser scans of the edge area - Preliminary results 1



- Metal pattern visible, step too large to see it clearly
- Charge collected from a region ~ 100µm outside the active area
- Charge collection in the top region not completely uniform ⇒ Software bug? (...investigating)

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Laser scans of the edge area - Preliminary results 2



FINE SCAN $@V_{bias} = -15V$ step=10 μm



Finer scan (10 μm step) of the lateral edge

- Edge length: $\sim 200 \mu m$
- ► Signal visible up to ~ 100µm away from the active area
- Metal pattern recognizable
- Electrodes are also visible (they are empty, less charge)

On the negative side:

- Slight misalignment of the detector
- ▶ Spot size ~ 10µm
- N⁺ and P⁺ electrodes had a poor alignment in this batch (ATLAS07) due the the high wafer bowing

Conclusions

- We fabricated and tested 3D detectors with passing through columns and slim-edge
- The slim-edge showed to be a very good alternative to the active-edge (easier fabrication, and good reduction of the edge dead area)
- ► It was understood that the slim-edge can be reduced to a total of 100µm
- ► Good charge collection efficiency along the edges (~ 100µm away from the active area)
- The slim-edge was also tested on irradiated pixel devices (up to 5 × 10¹⁵n.eq/cm⁻²) during June 2011 test beam at CERN with very good results
- We will continue the functional testing and optimization of the slim-edge while designing the next batch of Full 3D detectors at FBK



Thank you!



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