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## Connecting EPICS with Easily Reconfigurable I/O Hardware

*Wednesday, October 5, 2011 12:00 PM (15 minutes)*

### Summary

This presentation covers recent developments aimed at facilitating the integration of Reconfigurable I/O (FPGA-based) hardware with EPICS. The Linux driver available for National Instruments Reconfigurable I/O (NI-RIO) hardware provides the interface between the FPGA firmware and the host controller. The FPGA programming of these devices is accomplished using LabVIEW and can include custom VHDL code if needed. Support is currently available for basic record types, including waveform, and we will discuss briefly the implementation details as well as future possible improvements.

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