

Mu3e integration meeting

Tuesday, 19 June 2018

Electronics integration (14:00 - 17:25)

time	[id] title	presenter
14:00	[8] Welcome	
14:10	[1] Integration status	STREULI, Silvan
14:30	[9] Powering status	Dr WIEDNER, Dirk
14:50	[10] Frontend board status	Dr WIEDNER, Dirk
15:10	[11] Power cabling inside detector	
15:30	[12] Readout cables inside detector	MEIER, Beat Dr WIEDNER, Dirk DITTMEIER, Sebastian
15:50	[13] SciFi boards status	Dr BRAVAR, Alessandro
16:10	[14] SciTile boards status	Mr BRIGGL, Konrad Dr MUNWES, Yonathan
16:30	[15] Pixel boards status	
16:55	[16] Discussion, wrap-up, next steps	